

5

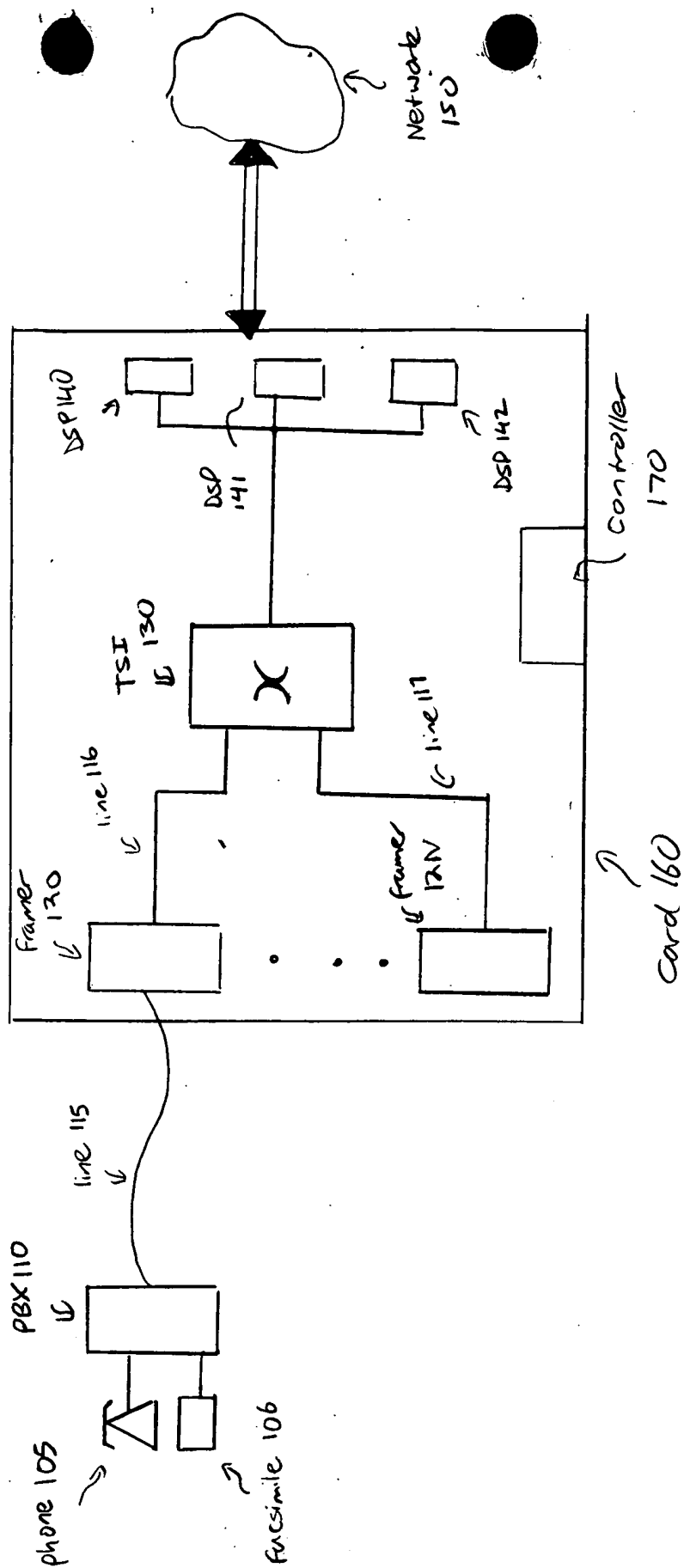
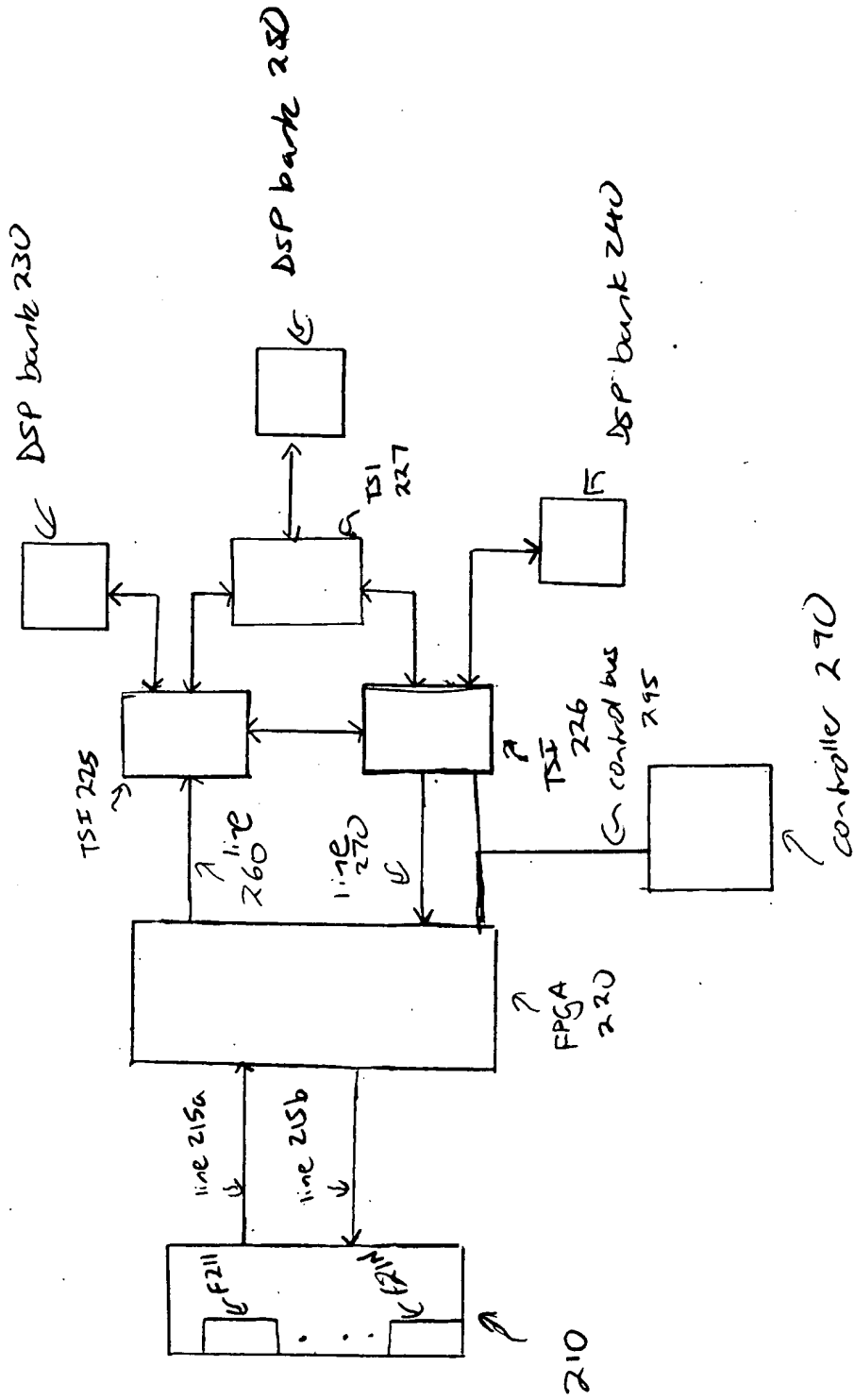


Figure 1 (PRIOR ART)



003

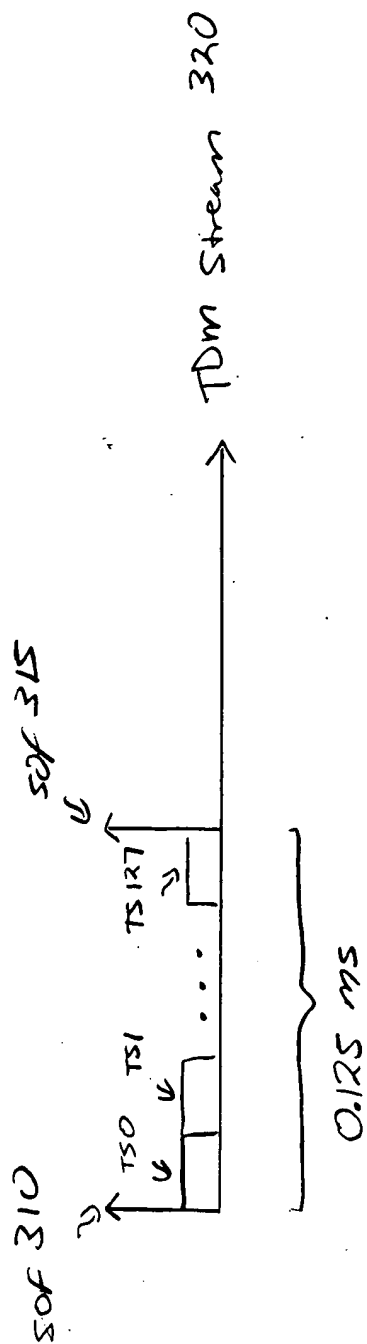


Figure 3

66200-004288
FPGA 400

✓

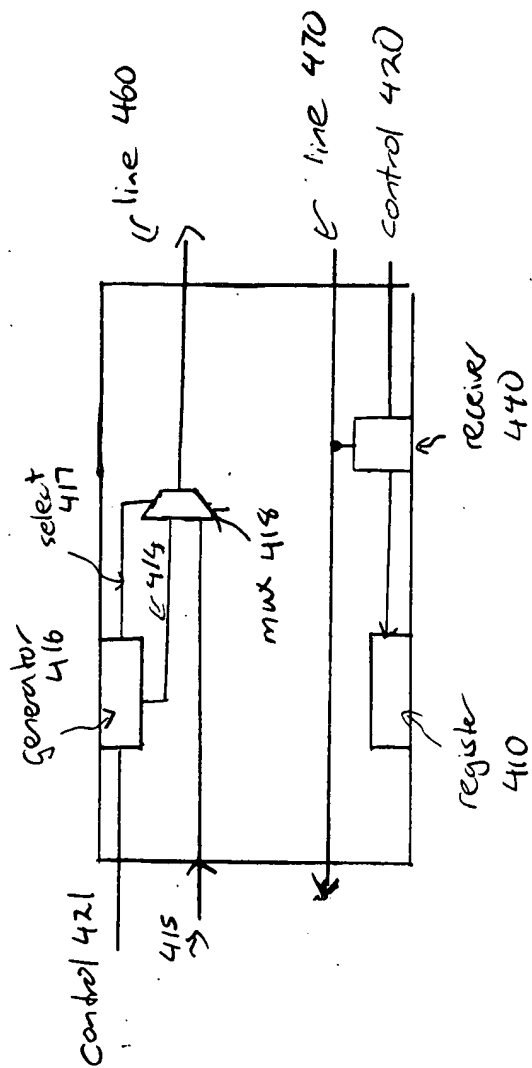


Figure 4



22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS

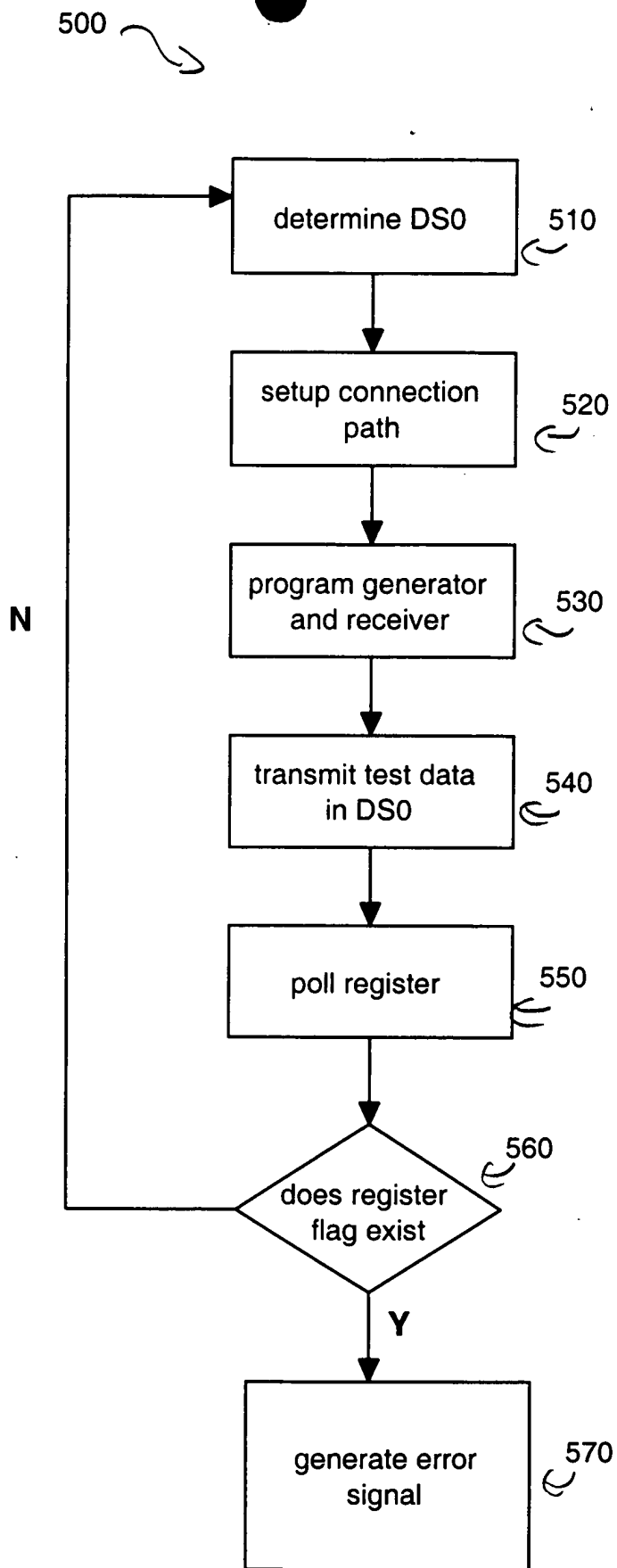


Figure 5

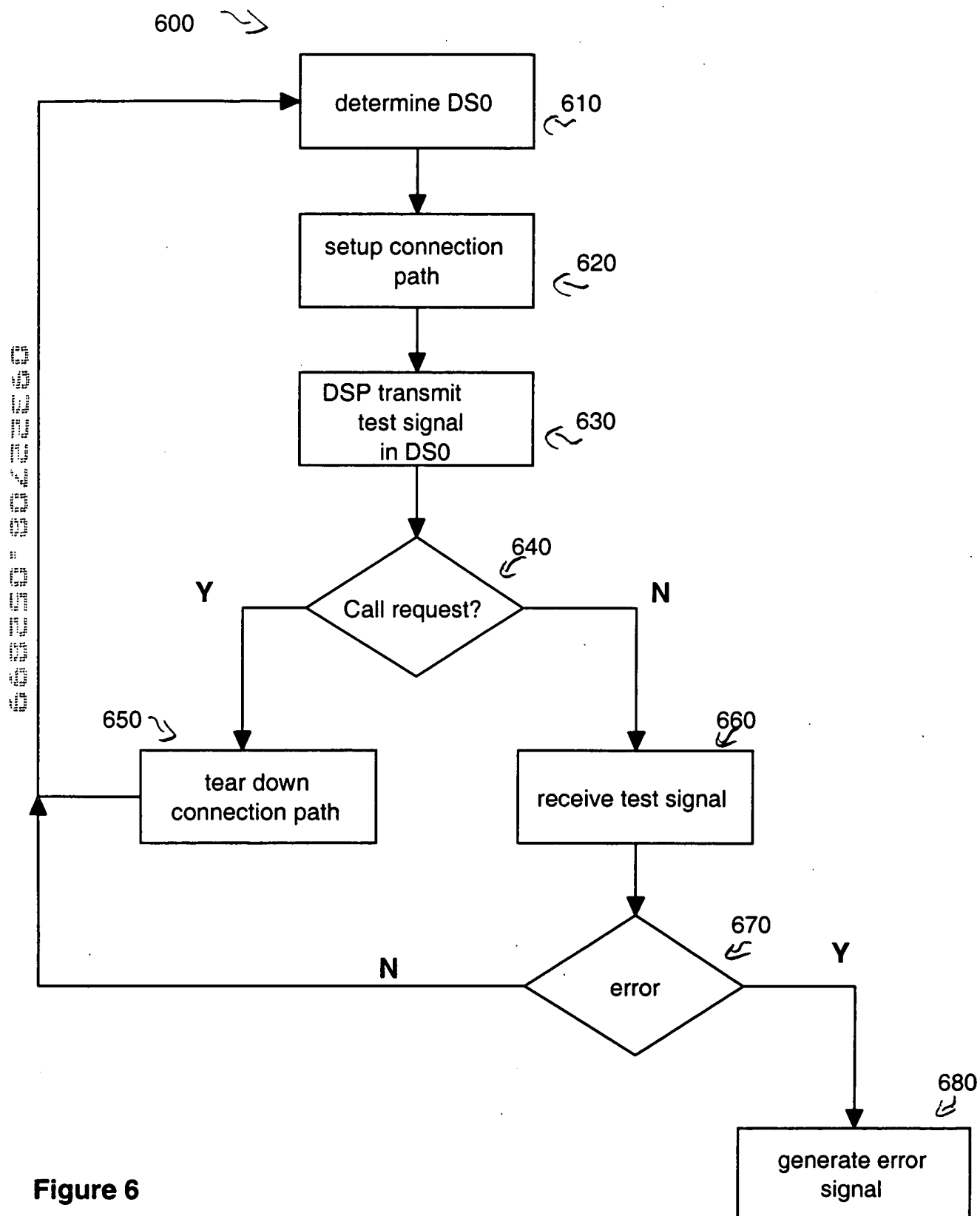


Figure 6